

IB-XS-1W Series

FIXED INPUT,ISOLATED®ULATED Single Output DC/DC Converter



FEATURES

◆RoHS	compliant

- ◆Efficiency up to 86%
- ◆SIP Package
- ◆Wide temperature performance at full
 - 1 Watt load,-40°C to 85°C
- ♦UL 94V-0 package material
- ◆No heatsink required
- ◆Small Footprint
- Industry standard pinout
- ◆Power sharing on output
- ◆1KVDC isolation
- ◆Continuous Short Circuit Protection
- ◆Internal SMD construction
- ◆No external components required
- ◆MTTF up to 1.8 million hours

MODEL SELECTION <u>IB⁰05⁰05⁰X⁰S⁰-1W⁰</u>

- ①Product Series 3 Output Voltage
- 2 Input Voltage
- (4) Fixed Input
- **⑤SIP Package**
- 6 Rated Power

APPLICATIONS

The IB_XS-1W series are specially designed for applications where a group of polar power supplies are isolated from the input power supply in a distributed power supply system on a circuit board.

These products apply to:

- 1) where the voltage of the input power supply is fixed (voltage variation ≤±5%);
- 2) where isolation is necessary between input and output (isolation voltage ≤1000VDC);
- 3) where the regulation of the output voltage and the output ripple noise are demanded.

RoHS	CE REACH
MICRODE Green Technolgy	MICRODC RESERVES THE COPYRIGHT

SELECTION GUIDE							
Order code	Input Voltage(VDC)		Voltage	Output Currer	nt(MA)	Efficiency	Switching Frequency
	Nominal	Range	(VDC)	Max	Min	(%,Typ)	(KHz,Typ)
IB0505XS-1W	5	4.75-5.25	5	100	10	70	100
IB1205XS-1W	12	11.4-12.6	5	100	10	69	75
IB1505XS-1W	15	14.25-15.	5	100	10	71	83
IB2405XS-1W	24	22.8-25.2	5	100	10	72	67

ISOLATION SPECIFICATIONS						
Parameter	Test conditions	Min.	Тур.	Max.	Units	
Isolation test voltage	Flash tested for 1 minute and 1mA max	1000			VDC	
Isolation resistance	Test at Viso=500VDC	1000			МΩ	

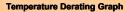
OUTPUT SPECIFICATIONS					
Parameter	Test conditions	Min	Тур.	Max.	Units
Output power		0.1		1	W
Line regulation	For Vin change of ±5%			±0.25	%
Load regulation	10% to 100% full load			±1	%
Output voltage accuracy	100% full load			±3	%
Temperature drift	100% full load			0.03	%/°C
Output Ripple*	20MHz Bandwidth		10	20	MV p-p
Output Noise*	20MHz Bandwidth		50	100	MV p-p
Switching frequency	Full load,nominal input		100		Khz

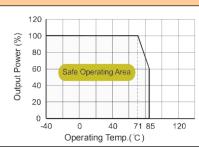
^{*} Test ripple and noise by "parallel cable" method. See detailed operation instructions at Testing of Power Converter section, application notes.



TEMPERATURE CHARACTERISTICS Units Parameter Conditions Min Тур. Max Storage humidity range 95 % Operating temperature -40 80 °C Storage temperature -55 125 °C Lead temperature 1.5mm from case for 10 seconds 30 °C 20 Temp.rise at full load 300 °C Cooling Free air convection Plastic(UL94-V0) Case material Continuous Short circuit protection s MTBF 3500 K hours Weight 2.8 a

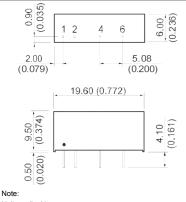
TYPICAL CHARACTERISTICS





OUTLINE DIMENSIONS & PIN CONNECTIONS

SIZE Graph





Pin section:0.50*0.3mm(0.020*0.012inch) Pin section tolerances: ± 0.10 mm(± 0.004 inch)

General tolerances: \pm 0.25mm(\pm 0.010inch)



FOOTPRINT DETAILS				
Pin	Function			
1	VIN			
2	GND			
4	0V			
6	+V0			

All specifications typical at TA=25°C, nominal input voltage and rated output current unless otherwise ecified Another 24V products, please inquire Our technical department

Requirement on output load

To ensure this module can operate efficiently and reliably, During operation, the minimum output load is not less than 10% of the full load, and that this product should never be operated under no load! If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load.or use our company's products with a lower rated output power.

Professional Power Module

Microdo Professional Power Module Inc. Tel:0086-20-86000646 E-mail:tech@microdc.cn Website:http://www.microdc.cn



ROHS COMPLIANT INFORMATION

This series is compatible with RoHS soldering systems with a peak wave solder temperature

of 300° C for 10 seconds.

The pin termination finish on the SIP package type is Tin Plate, Hot Dipped over Matte Tin with Nickel Preplate. The DIP types are Matte Tin over Nickel Preplate. Both types in this series are backward compatible with Sn/Pb soldering systems.

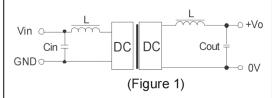
REACH COMPLIANT INFORMATION

This series has proven that this product does not contain harmful chemicals, it also has harmful chemical substances through the registration, inspection and approval.

IB-XS-1W Series

Recommended circuit

If you want to further decrease the input/output ripple, an "LC" filtering network may be connected to the input and output ends of the DC/DC converter, see (Figure 1).



It should also be noted that the inductance and the frequency of the "LC" filtering network should be staggered with the DC/DC frequency to avoid mutual interference. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor sees (Table 1).

EXTERNAL CAPACITOR TABLE (TABLE 1)

Vin	Cin	Vout	Cout
(VDC)	(μ F)	(VDC)	(μ F)
5	4.7	5	4.7
12	2.2	-	-
15	1	-	-
24	0.47	-	-

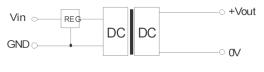
It's not recommend to connect any external capacitor in the application field with less than 0.5 watt output.

Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against over-current and short-circuits. The simplest method is to connect a self-recovery fuse in series at the input end or add a circuit breaker to the circuit.

Input Over-voltage Protection Circuit

The simplest device for input over-voltage protection is a linear voltage regulator with overheat protection that is connected to the input end in series (Figure 2).



(Figure 2)

When the environment temperature is higher than 71 °C the product output power should be less then 60% of the rated power.

No parallel connection or plug and play.

Use dual output simultaneously, forbid opening output pin (0V) to use as single output.

^{*}Supply voltage must be discontinued at the end of short circuit duration.